

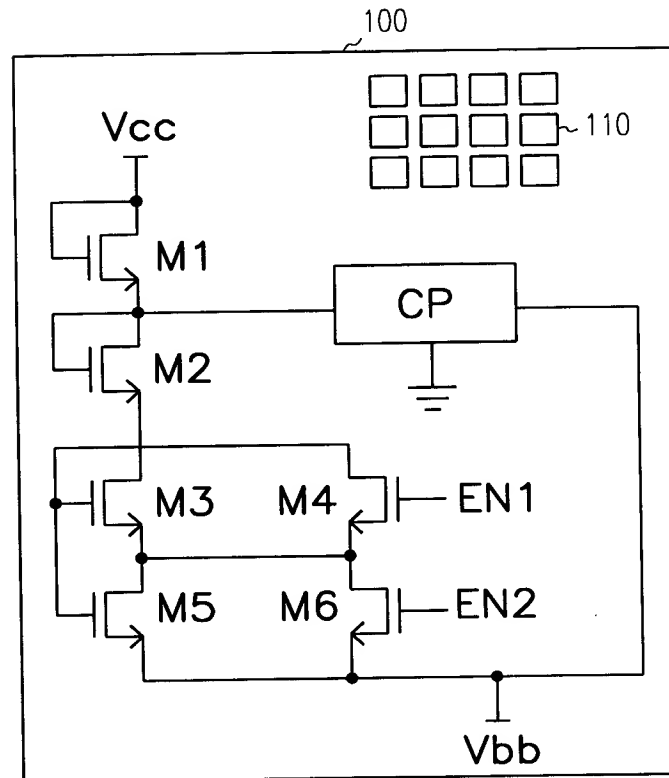


TITLE: ON-CHIP SUBSTRATE REGULATOR TEST MODE  
INVENTOR NAME: C. B. Gilliam

INVENTOR NAME: Gary R. Gilliam

SERIAL NO.: 09/934,784

REPLACEMENT SHEET

 $1/3$ 



TITLE: ON-CHIP SUBSTRATE REGULATOR TEST MODE  
INVENTOR NAME: Gary R. Gilliam  
SERIAL NO.: 09/934,784

2/3

$$\begin{aligned}V_{GS} &= 1 - V_S \\V_{DS} &= -1.0 - V_S \\V_{GS} - V_{DS} &= 2 \\V_{GS} &= V_{DS} + 2\end{aligned}$$

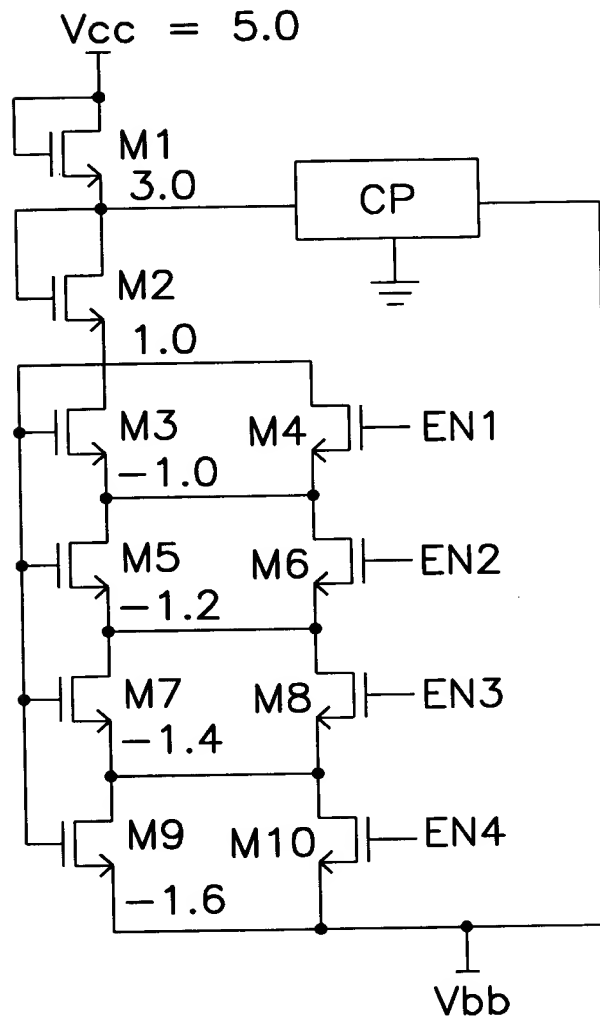


FIG. 2

FIG. 3

